



FIG.1
SYSTEM CONFIGURATION

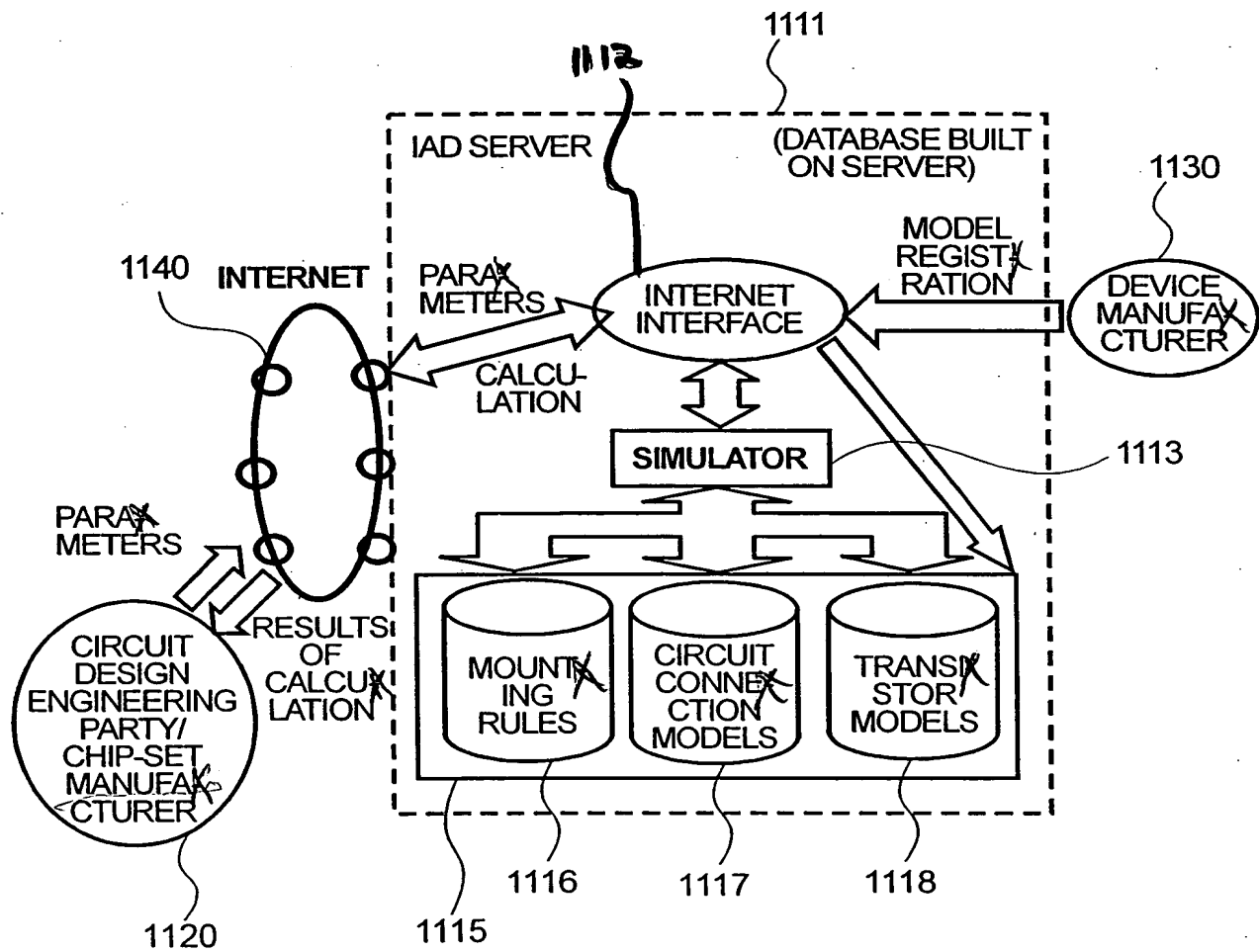
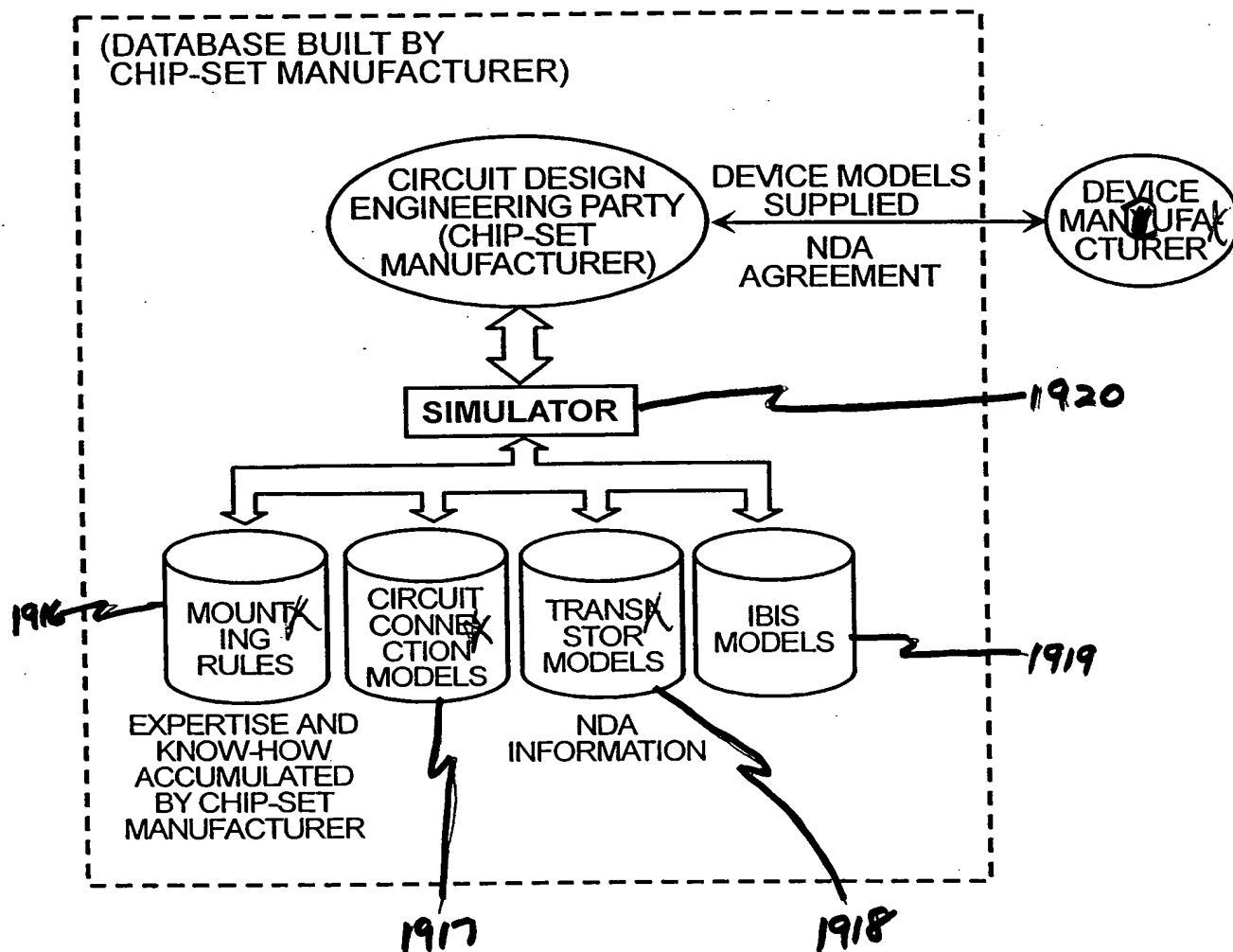


FIG.9

CONVENTIONAL SYSTEM CONFIGURATION



The diagram illustrates a system architecture for circuit design, centered around an IAD SERVER (DATABASE BUILT ON SERVER). The server contains an INTERNET INTERFACE (2111) and a SIMULATOR (2114). The INTERNET INTERFACE (2111) connects to the INTERNET (2112) and a MODEL REGISTRATION (2113). It sends PARAMETERS to the INTERNET (2112) and receives CALCULATION results. The INTERNET INTERFACE (2111) also sends data to the SIMULATOR (2114), which in turn sends data to three databases: MOUNTING RULES (2115), CIRCUIT CONNECTION MODELS (2116), and TRANSISTOR MODELS (2117). The SIMULATOR (2114) also receives data from TRANSISTOR MODELS A (2118a). The TRANSISTOR MODELS A (2118a) database is connected to TRANSISTOR MODELS C (2113) via a bidirectional arrow. The TRANSISTOR MODELS C (2113) database is connected to a DEVICE MANUFACTURER (2130). The TRANSISTOR MODELS A (2118a) database is also connected to a CIRCUIT DESIGN ENGINEERING PARTY/CHIP-SET MANUFACTURER (2120) via a bidirectional arrow. The CIRCUIT DESIGN ENGINEERING PARTY/CHIP-SET MANUFACTURER (2120) sends PARAMETERS MODELS to the INTERNET (2112) and receives RESULTS OF CALCULATION from the INTERNET (2112). The TRANSISTOR MODELS B (2118b) database is connected to the CIRCUIT DESIGN ENGINEERING PARTY/CHIP-SET MANUFACTURER (2120) via a bidirectional arrow.